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L5	2	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip and temp adj matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L6	309768	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip ans ici	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L7	101	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L8	1	(toeplitz adj matrix and (isi or (inter adj symbol adj interference))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37

L9	1524	375/233	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT;	OR	ON	2007/05/24 12:37
L10 4 toeplitz adj matrix with (isi or (inter adj symbol adj interference))		US-PGPUB; US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37	
L11 64 toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference)) and symbol			US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L12 16 (toeplitz adj matrix) and (dfe or (decision adj feedback adj equalizer)) and ici		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37	
L13	8	(toeplitz adj matrix) same (dfe or (decision adj feedback adj equalizer)) and (isi or (inter adj symbol adj interference))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L14	1	fff and fbf and wuc and wud	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L15	2	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip and averag\$3 with diagonal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L16	1	dfe with isi with ici and toeplitz	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37

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L17	7 8 (toeplitz adj matrix) same (dfe or (decision adj feedback adj equalizer))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L18	8 1 L9 and L12		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L19	. 52	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip and ici	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L20	0 8 L7 and L9		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L21	61	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and dfe	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L22	64	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L23	9	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference)) and (dfe or (decision adj feedback adj equalizer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L24	0	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference)) and (dfe or (decision adj feedback adj equalizer)) and wc and wud	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37

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L25	16	dfe with isi with ici	US-PGPUB;	OR	ON	2007/05/24 12:37
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L26	toeplitz adj matrix and (isi or (inter adj symbol adj interference))		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L27	L27 1 "10/665462"		US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L28	18	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference)) and symbol with based	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L29	18	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip and (dfe or (decision adj feedback adj equalizer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L30	13	(toeplitz adj matrix) and (isi with ici)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L31	6	(toeplitz adj matrix) same (isi or (inter adj symbol adj interference)) and (dfe or (decision adj feedback adj equalizer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L32	1	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (ici or (inter adj carrier adj interference)) and (dfe or (decision adj feedback adj equalizer)) and wuc and wud	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37

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L33	2	"7113553".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L34	62	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and (dfe or (decision adj feedback adj equalizer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L35	2	"6690715".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37
L36	2	toeplitz adj matrix and (isi or (inter adj symbol adj interference)) and chip and (dfe or (decision adj feedback adj equalizer)) and (averag\$5 with diagonal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/24 12:37

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ISI. Inter-Symbol Interference. LA. Layered Architectures. LBR. Low Bit Rate. MA. Multiple

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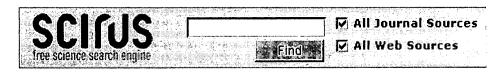
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لا	•	Erdogan, Alper Tunga / Halder, Bijit / Sang, 1		STATES PATENT	

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...Only Memory [0070] EC—Echo Canceller [0071] ECF—Echo Cancellation...Services Digital Network [0099] ISI—Inter Symbol Interference...Complexity Dual Rate Echo Canceller [0132] RMB—Rate Matching...0159] WEVE-EC—Weighted Vector Error Echo Canceller (DETAILED DESCRIPTION OF...

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MA, CHINGWO	DANVILLE	CALIFORNIA
LIN, JEFF	TAIPEI	TAIWAN
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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10665462	Not Issued	41		Method for updating coefficients in decision feedback equalizer	CHEN, TIEN-HUI
11224757	Not Issued	30		Circuit for improving channel impulse response estimation and compensating for remnant frequency offset in the orthogonal frequency division multiplexing baseband receiver for IEEE 802.11a/g wireless LAN standard standard	CHEN, TIEN-HUI
11235714	Not Issued	30	09/26/2005	Method and circuit for timing recovery	CHEN, TIEN-HUI
11463913	Not Issued	41		METHOD FOR NETWORK DIAGNOSTIC	CHEN, TIEN-HUI

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
08612691	<u>5748126</u>	150	03/08/1996	SIGMA-DELTA DIGITAL-TO- ANALOG CONVERSION SYSTEM AND PROCESS THROUGH RECONSTRUCTION AND RESAMPLING	MA, CHINGWO
10402154	7164731	150	03/31/2003	APPARATUS AND METHOD OF ADAPTIVE FREQUENCY OFFSET ESTIMATIONS FOR A RECEIVER	MA, CHINGWO
10665462	Not Issued	41		Method for updating coefficients in decision feedback equalizer	MA, CHINGWO
10710262	Not Issued	30		Radio Receiver Supporting Multiple Modulation Formats with a Single Pair of ADCs	MA, CHINGWO
11250669	Not Issued	30	10/14/2005	Method and circuit for frequency offset estimation in frequency domain in the orthogonal frequency division multiplexing baseband receiver for IEEE 802.11A/G wireless LAN standard	MA, CHINGWO
60483115	Not Issued	159	06/30/2003	Radio receiver supporting multiple modulation formats with a single pair of ADCs	MA, CHINGWO

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Application#	Patent#	Status	Date Filed	litte	Inventor Name
10665462	Not Issued	41		Method for updating coefficients in decision feedback equalizer	KAO, KAI-PON
10952035	7187915	150	09/28/2004	AUTOMATIC GAIN CONTROL FOR A WLAN SYSTEM	KAO, KAI-PON
11215216	Not Issued	30	08/30/2005	Method and circuit for fine timing synchronization in the orthogonal frequency division multiplexing baseband receiver for IEEE 802.11a/g wireless LAN standard	KAO, KAI-PON
11224757	Not Issued	30	09/13/2005	Circuit for improving channel impulse response estimation and compensating for remnant frequency offset in the orthogonal frequency division multiplexing baseband receiver for IEEE 802.11a/g wireless LAN standard standard	KAO, KAI-PON
11229934	Not Issued	30		Method and system for assigning a receiving antenna	KAO, KAI-PON
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